1   **PROCESSOR STATE AWARE INTERRUPTS FROM PERIPHERALS**

2   <u>ABSTRACT OF THE INVENTION</u>

3   A method, apparatus and computer product wherein
4   interrupt thresholds are automatically adjusted based
5   on the current state of the processor. The processor
6   provides an output signal, possibly on one or more
7   lines, that is indicative of the state the processor is
8   in such as active, idle sleep. The peripherals monitor
9   this signal and their interrupt thresholds are varied
10   to be low when the processor is active and to be high
11   when the processor is asleep. This causes the
12   peripherals to delay their respective interrupts when
13   the processor is asleep. When the processor is
14   awakened, all peripherals requiring it may be serviced.